



Z80 Microprocessor Architecture, Interfacing, Programming and Design [HB]-3/e

Ramesh Gaonkar

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Z80 microprocessor book is intended for microprocessor courses at the undergraduate level in technology, engineering, and computer science. Now this book in its third edition, provides a comprehensive treatment of the microprocessor, covering both hardware and software based on the Z80 microprocessor family. Part I deals the hardware aspects of the Z80 processor and Part II includes assembly language programming with many illustrative programs. Part III synthesizes the hardware concepts of Part I and software techniques of Part II. It includes advanced topics in interfacing memory and I/Os with numerous industrial and practical applications. Each illustration analyzes the hardware and software and describes how hardware and software work together to accomplish design objectives.



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