



PLL Performance, Simulation, and Design

Dean Banerjee

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This book is the third edition and is intended for the reader who wishes to gain a solid understanding of PLL frequency synthesizers. There are other applications and implementations for the PLL, for which this book is not intended. This exclusive focus on current technology and frequency synthesizer applications allows the reader to develop a much better understanding of these concepts.

This book takes a unique approach to the study of the phased lock loop in frequency synthesizer applications by using the rare combination of rigorous mathematical derivation and actual measured results. Only when these two things are in agreement can one claim to really understand the system.

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